

FIG. 1

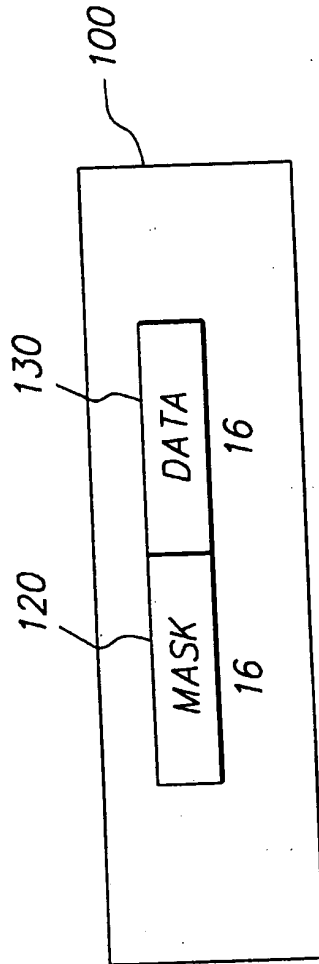


FIG. 2

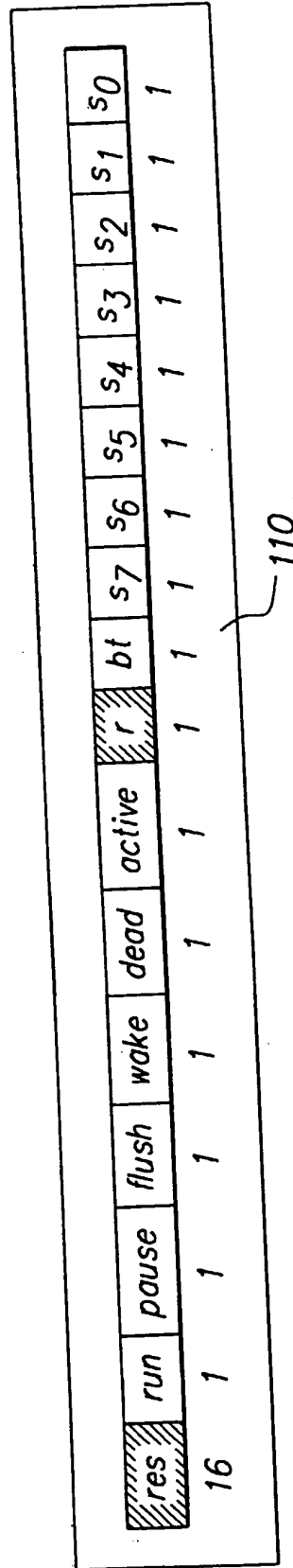


FIG. 3

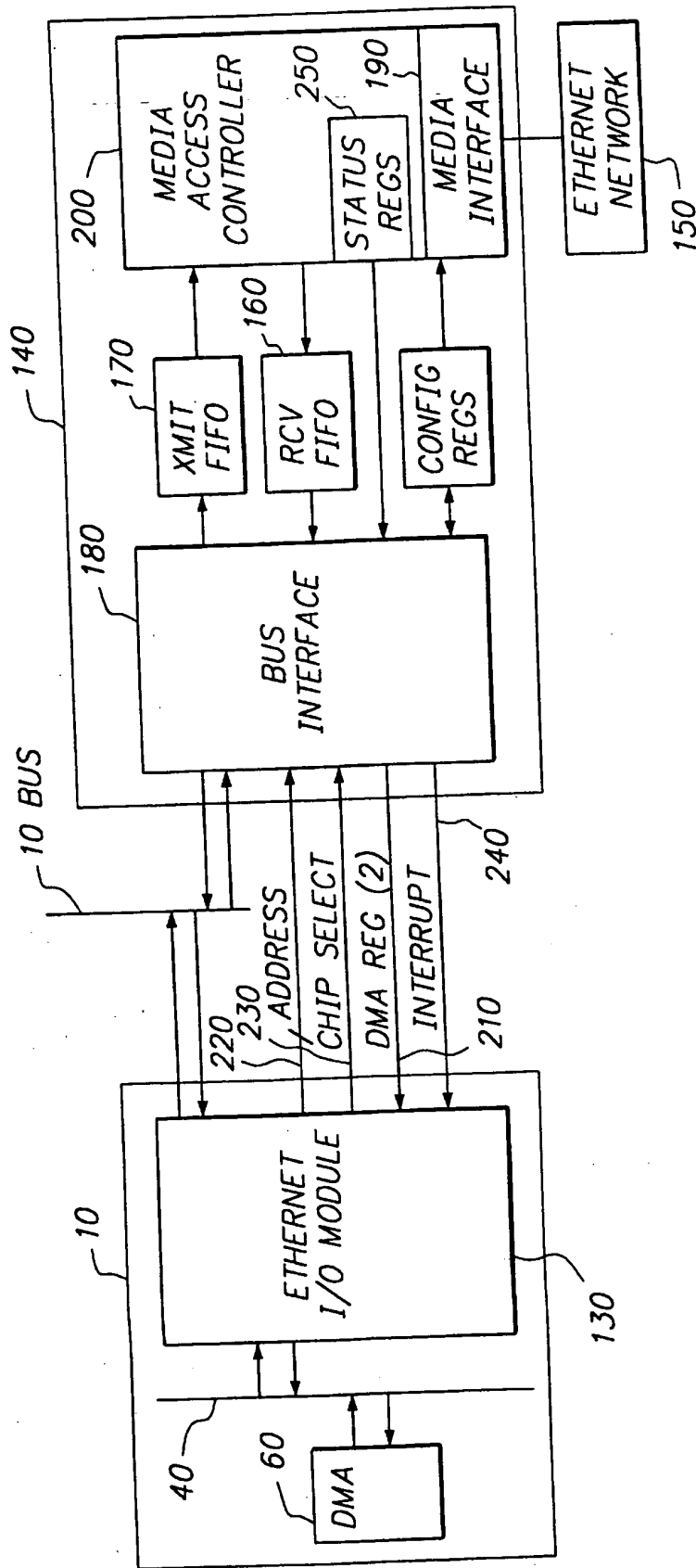
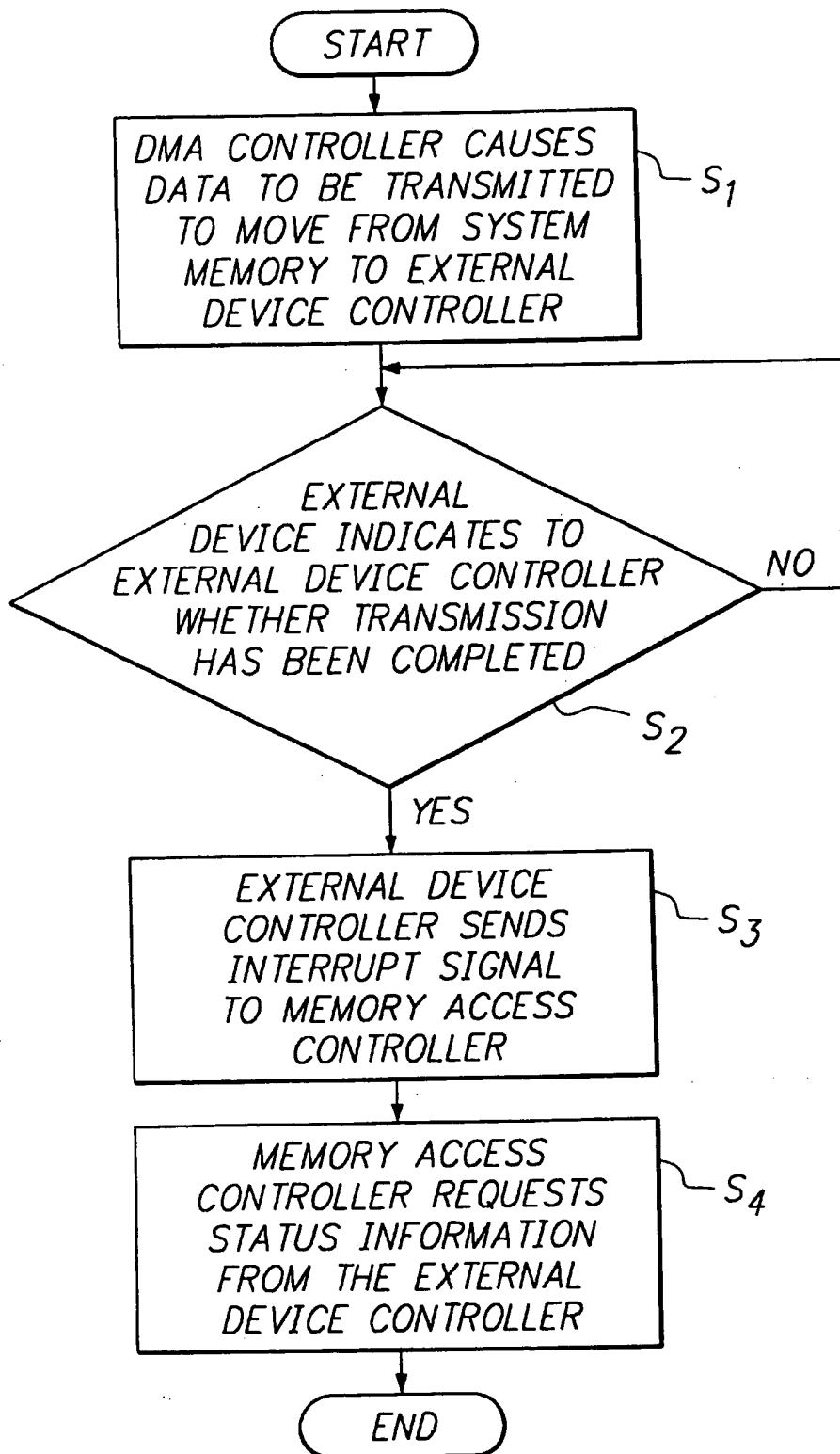


FIG. 4



**FIG. 5**

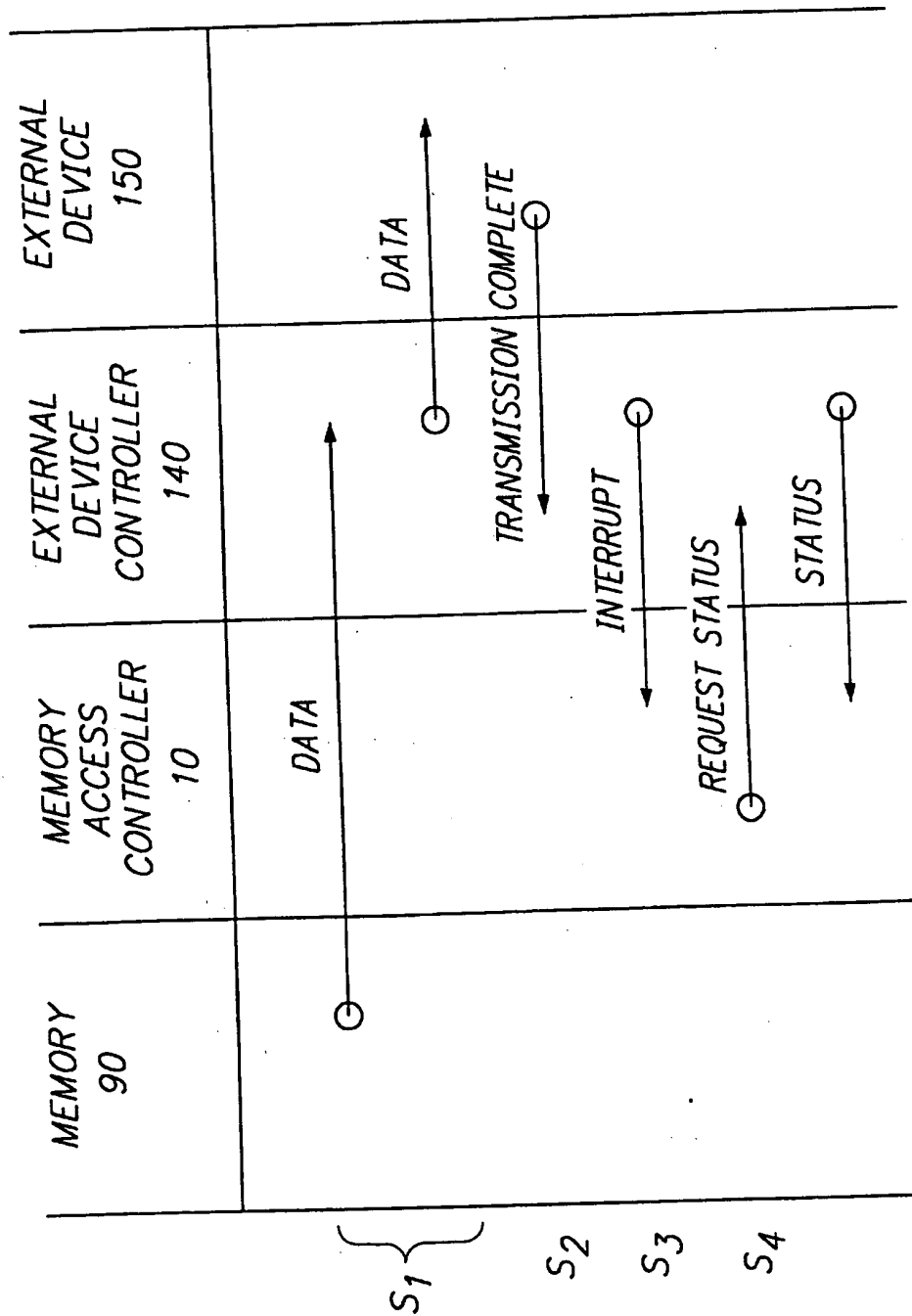


FIG. 6

<i>BIT</i>	<i>MEANING</i>
<i>S<sub>7</sub></i>	<i>ERROR</i>
<i>S<sub>6</sub></i>	<i>NOT IMPLEMENTED</i>
<i>S<sub>5</sub></i>	<i>MACE INTERRUPT</i>
<i>S<sub>4</sub>...S<sub>1</sub></i>	<i>NOT IMPLEMENTED</i>
<i>S<sub>0</sub></i>	<i>WAIT (EXTERNALLY CONTROLLED)</i>

**FIG. 7**

<i>BIT</i>	<i>MEANING</i>
<i>S<sub>7</sub></i>	<i>NOT IMPLEMENTED</i>
<i>S<sub>6</sub></i>	<i>EOP (END OF PACKET)</i>
<i>S<sub>5</sub>...S<sub>1</sub></i>	<i>NOT IMPLEMENTED</i>
<i>S<sub>0</sub></i>	<i>WAIT (EXTERNALLY CONTROLLED)</i>

**FIG. 9**

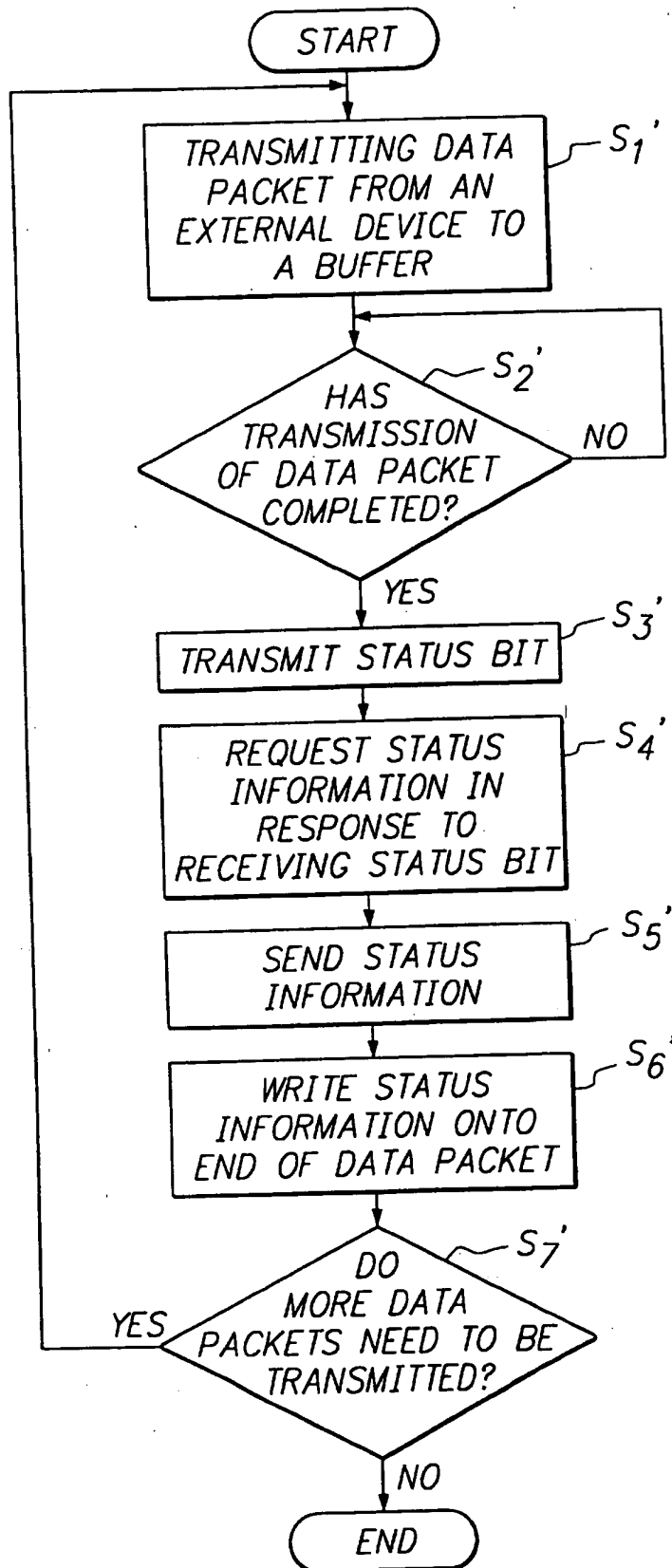


FIG. 8